

**UNIVERSITY COLLEGE OF ENGINEERING (Autonomous)**  
**OSMANIA UNIVERSITY, HYDERABAD**  
**M.E. (E.C.E) II - Semester (Regular & PTPG) Main Examination**  
**July/August 2016**

**Examination Centre:**  
**Department of ECE,**  
**UCE (A), O.U.**

**Re-REVISED EXAMINATION TIME TABLE**

**Time : 2.00 PM to 5.00 PM**

Date and Day	Digital Systems		Systems & Signal Processing		Microwave & Radar Engineering		Embedded Systems and VLSI Design
	Regular II-Semester	PTPG II-Semester	Regular II-Semester	PTPG II-Semester	Regular II-Semester	PTPG II-Semester	Regular II-Semester
25-07-2016 MONDAY	ASIC Verification with System Verilog	ASIC Verification with System Verilog	Digital Spectral Analysis	Digital Spectral Analysis	Radar Systems Engineering	=====	VLSI Physical Design
27-07-2016 WEDNESDAY	Advanced Communication & Computer Networks	Advanced Communication & Computer Networks	Image & Video Processing	=====	Microwave Integrated Circuits	Microwave Integrated Circuits	Scripting Languages for VLSI Design Automation
29-07-2016 FRIDAY	Low Power VLSI Design	=====	Optical Fiber Communication Systems	=====	Optical Fiber Communication Systems	=====	Operating System for Embedded Systems
02-08-2016 TUESDAY	<b>Modern Digital Signal Processing</b>	=====	=====	=====	Microwave Antennas	Microwave Antennas	=====
04-08-2016 THURSDAY	VLSI Design & Technology	VLSI Design & Technology	Adaptive Signal Processing	Adaptive Signal Processing	Microwave Solid State Devices and Applications	=====	Embedded System Programming
06-08-2016 SATURDAY	=====	Advanced Computer Networks	Optimization Techniques	=====	Optimization Techniques	Global Navigational Satellite Systems	Optimization Techniques
08-08-2016 MONDAY	Coding Theory & Techniques	Engineering Research Methodology	Coding Theory & Techniques	Coding Theory & Techniques	Satellite Radio Navigation	Satellite Radio Navigation	VLSI Technology
10-08-2016 WEDNESDAY	System on Chip Architecture	=====	<b>Adv. Computer Organization</b>	=====	Engineering Mathematics	Electromagnetic Field Theory	=====

**Note: 1. This Time-Table is also applicable for Pre-Ph.D. students.**  
**2. Backlog students are eligible to appear without Re-registration of the subjects as per this Timetable.**



**DIRECTOR OF EVALUATION**  
**EXAMINATION CELL**